

SF400

! " #				
\$%&'	SF400			
! () *	V1.00	+, - .	2019.11	
/ O 1	234	561	789	
: ; < =				
() *	: ; > ?	@; 1	561	56+, - .
V1.00	! ()	234	789	2019.11.22
V1.01	ABCDEF 2G@;	234	789	2020.6.1
V1.02	ABCDEF 2G@;	234	789	2020.7.3
V1.03	@; WOL H NCSI I J	234	789	2021.1.7
V1.04	: KL SF400 MNOPQRPCB () T 1.03 UVW 1.04 () X	234	789	2021.6.1
V1.05	@; NOY* RZB[\] ^@; X	234	789	2021.6.23
V1.06	_` NOY* I J Z B[\] ^X	234	789	2021.12.21

1. SF400

SF400

CPU

CPU CPU

SF400 100

PCI Express

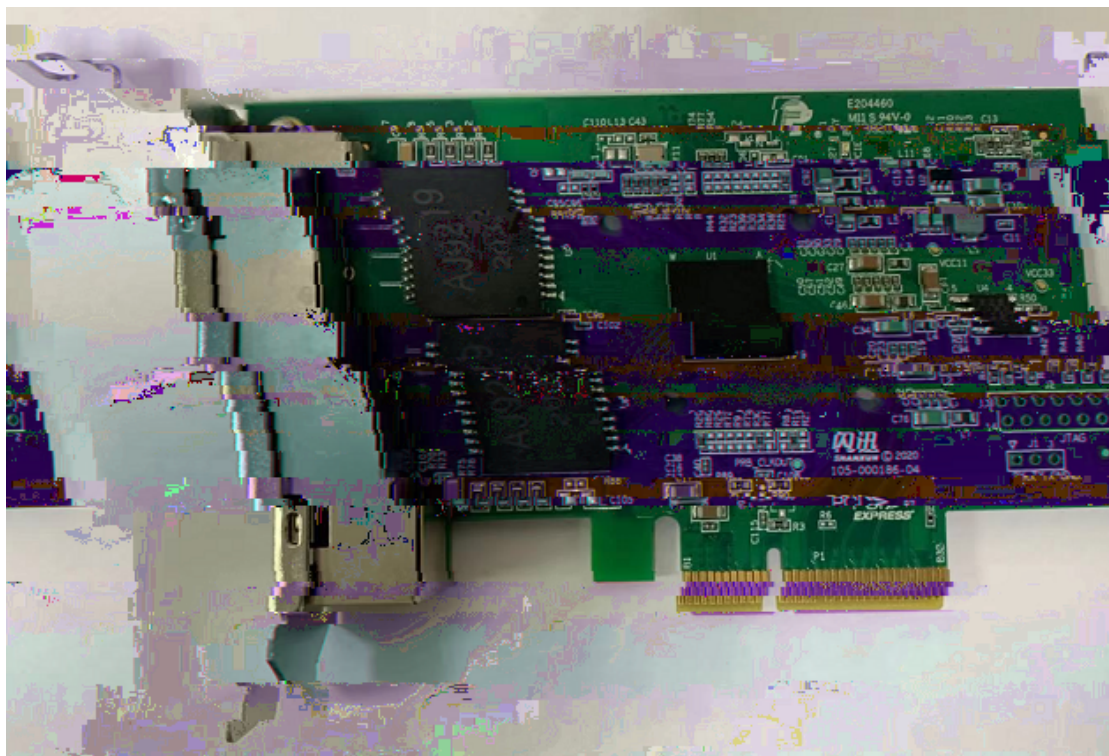
I/O

SF400 MAC+PHY

! " PCI Express # \$ % &

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-	
. / O	SF400T/SF400HT/SF400HXT(/1 /2)
3 4	5 3
6 3 7 /	10Base-T Category 3 4 5 5E or6, 8100m
	100Base-Tx Cat59Cat 5E and Cat6, 8100m
	1000Base-T Cat 5E and Cat6, 8100m
7 /	4 RJ45
. :	3W
PCIe (PCI Expressv2.1(5.0GT/s) x4; < x8=x16
>	10Mbps/100Mbps/1000Mbps (5 3)
6%? 6	Core6%1.1V _{+3%} ? 6 106%3.3V _{+3%} ? 6
@A	
6%	Yes
IEEE802.3	Yes
>	10BASE-T, 100BASE-TX, 1000BASE-T
BC	IEEE 802.3 az IEEE 802.3u 100Base-TX Fast Ethernet IEEE 802.1Q VLAN IEEE 802.1ad IEEE 802.3ab
DE F G(H)	SM2/SM3/SM4
PCI-SIG	Yes
I J	Yes
K L M N O (VMDq)	Yes
PXE	Yes
FCoE	NO
WOL	NO
NCSI	NO
LED PQRST	
LED UVP	1000/100M > ACT UVP 100M WVXY Z 100M WVX[Z ACT WVY Z &\]

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2.1 .

- Ø SF400
- Ø \$%
- Ø &' q
- Ø p (1 (1

2.2

X&% SF400) s* +, - . / O 12

- Ø 34 FM
- Ø 566%63
- Ø 7 M8 9 h: PCI-E X89X16 (;
- Ø 7 <=> 6?@A B; 8m +C
- Ø DE FGHI JK B q;
- Ø <> ?@L KM KM (
- Ø 8 M8 B 63 6N FM

2.3

O - APQ qRS TGU

. 3.

Ø . +, V WX6&Y

Ø Z[. \] | ` a +, ^^_

Ø W[` a bcd +, ef9gf9hi 9j f_

Ø -kl m, 9n79o'

Ø -k _pq rs 6tur &vw

Ø -k . xy rs 6tur &vw

Ø W[z{ rGz{ &vw .

Ø _k, 91 _k_|; |k, 0% } 90% } ~